



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hiroshi NAKAMURA

Serial No.: 10/083,552

Filed: February 27, 2002

For: VOLTAGE
GENERATING/TRANSFERRING CIRCUIT

Atty. Docket No.: 001701.00672

Group Art Unit: 2816

Examiner: A. Tra

Confirmation
No.: 8483

PRELIMINARY AMENDMENT UNDER 37 CFR 1.111

Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Final Office Action mailed November 5, 2002, kindly amend the above-referenced application as follows:

IN THE CLAIMS:

Kindly amend the claims as follows:

1. (Twice Amended) A voltage generating/transferring circuit comprising:
- a boost unit group including a plurality of boost units series-connected between input and output nodes;
 - a first transistor connected between the input node and a node for receiving a first voltage; and
 - a first capacitor having one end which is connected to the output node, and another end which receives a first oscillation signal,
- wherein each boost unit has input and output portions, a second transistor having a gate and a drain connected to the input portion and a source connected to the output portion, and a second capacitor in each boost unit connected to the input portion, and a gate of said first transistor is connected to the input portion of one of the boost units.